

I 8 Line Demultiplexer IC 74138

High Performance Digital IC Trainer Panel

The I 8 Line Demultiplexer IC 74138 trainer is designed to help students understand the working principle and practical applications of the 74138 3-to-8 Line Decoder/Demultiplexer IC.



8 Independent
Outputs (Y0 - Y7)



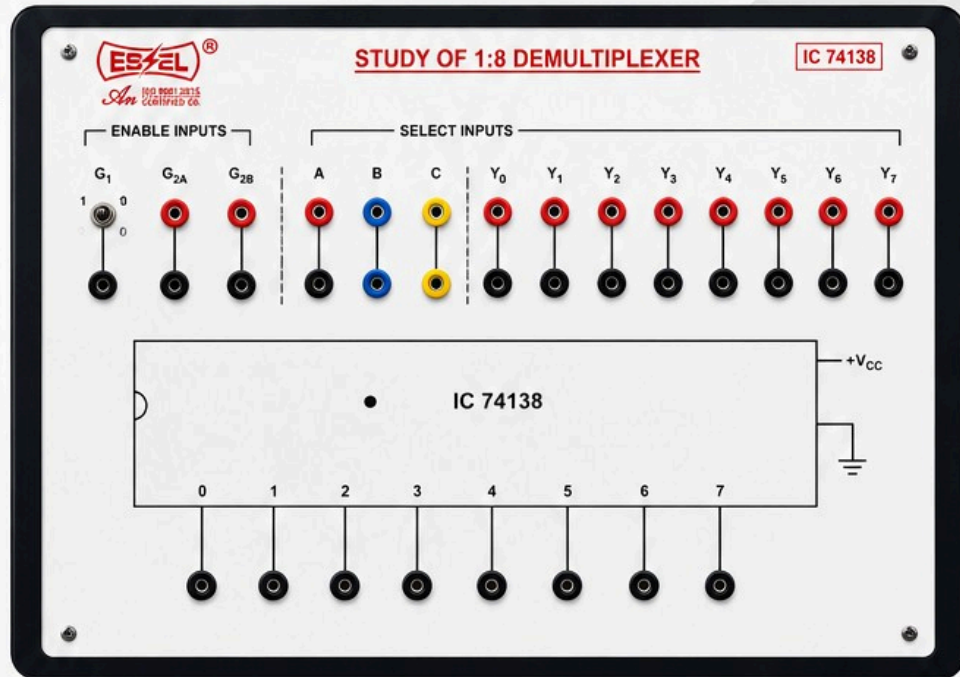
3 Select Inputs
(A, B, C)



3 Enable Inputs
(G₁, G_{2A}, G_{2B})



Ideal for Learning &
Testing Digital
Demultiplexing



ABOUT IC 74138

The 74138 is a high-speed TTL 3-to-8 line decoder/demultiplexer. It accepts three binary select inputs and activates one of eight outputs. The device includes three enable inputs, making it suitable for address decoding, memory selection, and digital logic applications.

It is widely used in:

- Digital Electronics Laboratories
- Logic Design Experiments
- Memory Address Decoding
- Microprocessor Interfacing
- Educational & Research Labs

FEATURES

- ✓ Complete study of 1:8 Line Demultiplexer operation
- ✓ Built around IC 74138
- ✓ 8 output lines available for observation
- ✓ 3 Select Inputs (A, B, C)
- ✓ Three Enable Inputs
- ✓ LED indication (where applicable)
- ✓ Clearly printed circuit diagram
- ✓ Heavy-duty ABS front panel
- ✓ High-quality banana sockets
- ✓ Regulated power supply
- ✓ Ideal for Engineering Colleges, Polytechnics & Technical Institutes

SPECIFICATIONS

	IC Used	74138
	Function	3-to-8 Line Decoder / Demultiplexer
	Number of Outputs	8 (Y0 - Y7)
	Select Inputs	3 (A, B, C)
	Enable Inputs	3 (G ₁ , G _{2A} , G _{2B})
	Supply Voltage	+5V DC
	Logic Family	TTL
	Operating Voltage	4.75V - 5.25V DC
	Output Type	Active LOW Outputs
	Front Panel	Printed Circuit Layout
	Connectors	4 mm Banana Sockets
	Cabinet	Powder Coated Metal Cabinet
	Dimensions (Approx.)	360 × 220 × 50 mm
	Weight (Approx.)	1.2 kg

APPLICATIONS

- Digital Logic Experiments
- Decoder & Demultiplexer Study
- Address Decoding
- Microprocessor Labs
- Embedded System Education
- Engineering & Polytechnic Laboratories

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